

PRIOR ART

Fig. 1

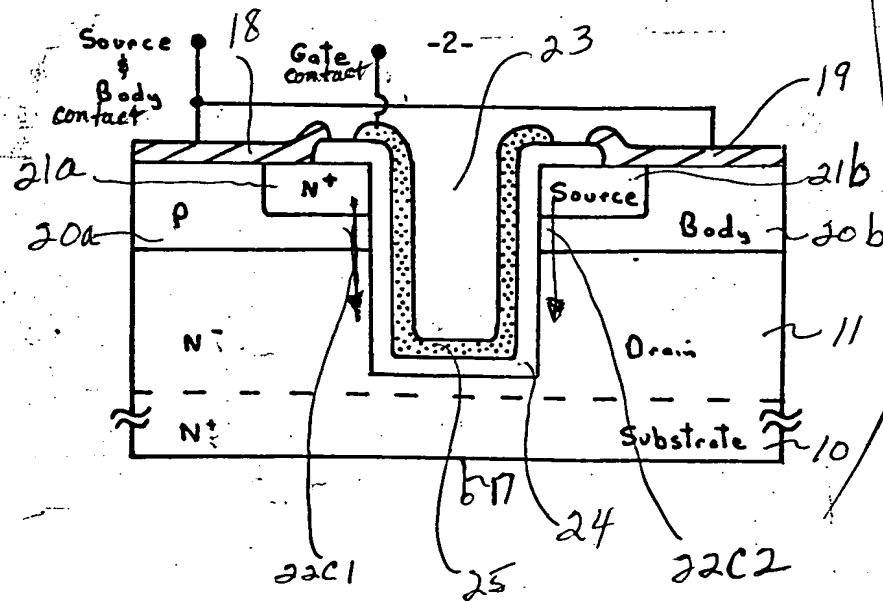
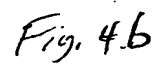
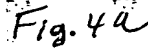
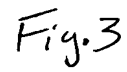


Fig. 2

PRIOR ART



[illegible]

Fig 4d

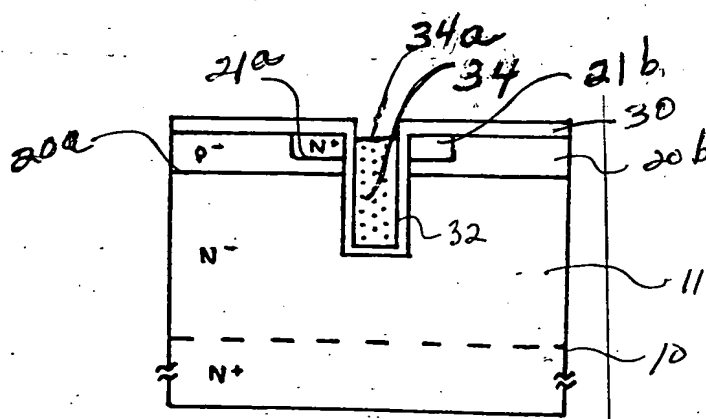


Fig 4e

M-300

CHIEF FILE

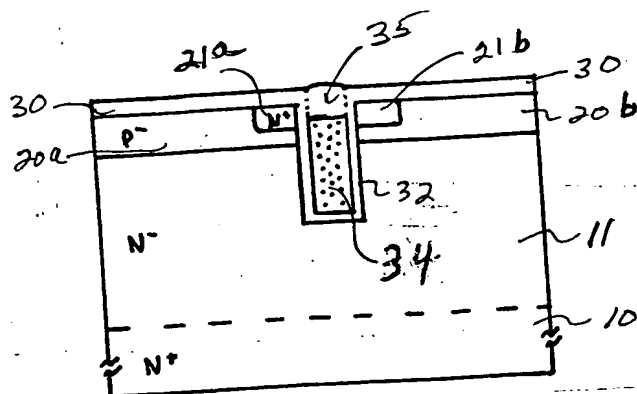


Fig 4f

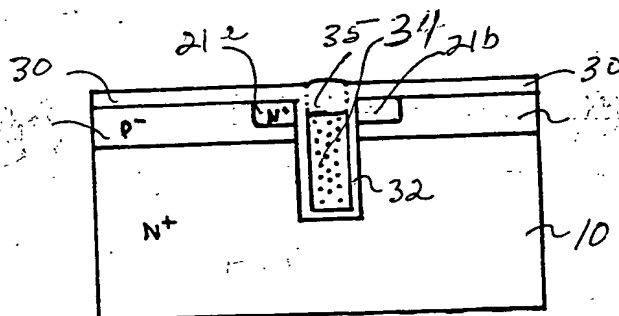


Fig 5

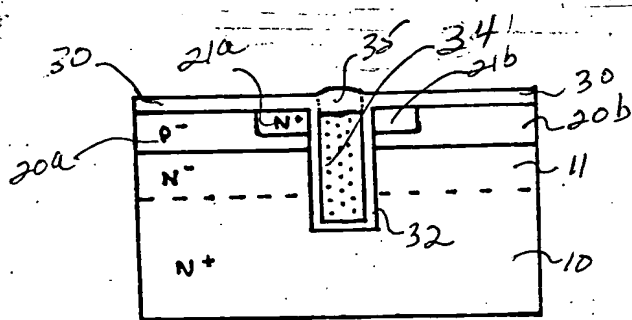


Fig 6

M-300

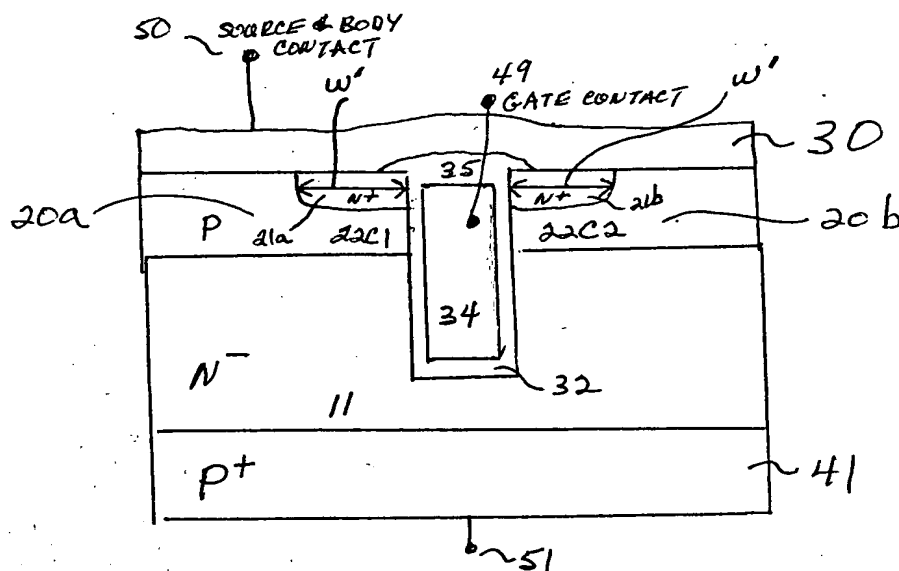


Fig. 7 Cross-section of an insulated gate transistor

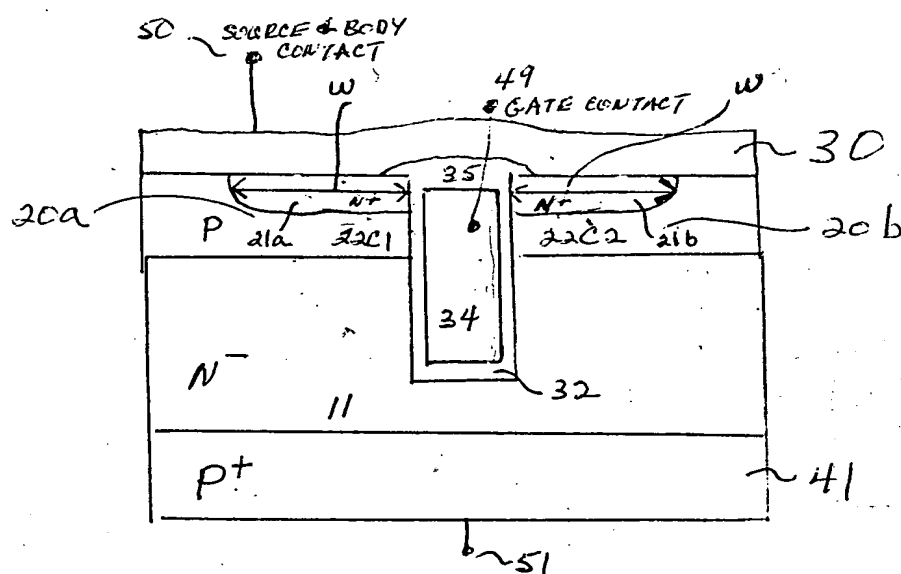


Fig. 8 Cross-section of a MOS-gated SCR

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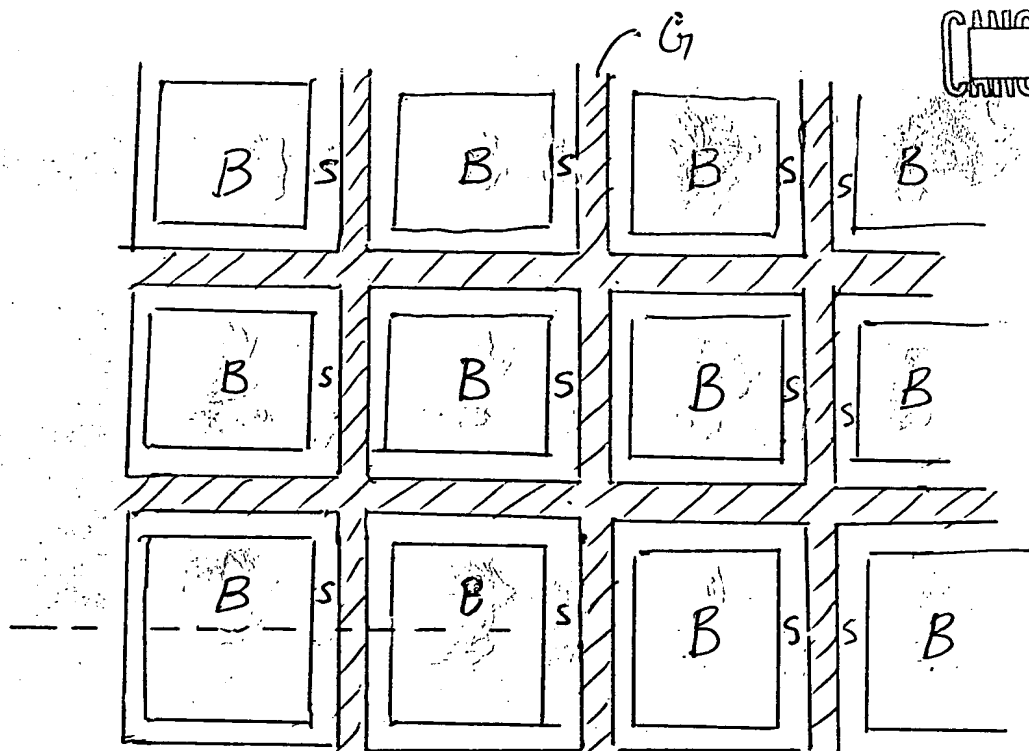


Fig. 9

M-300